

2/5

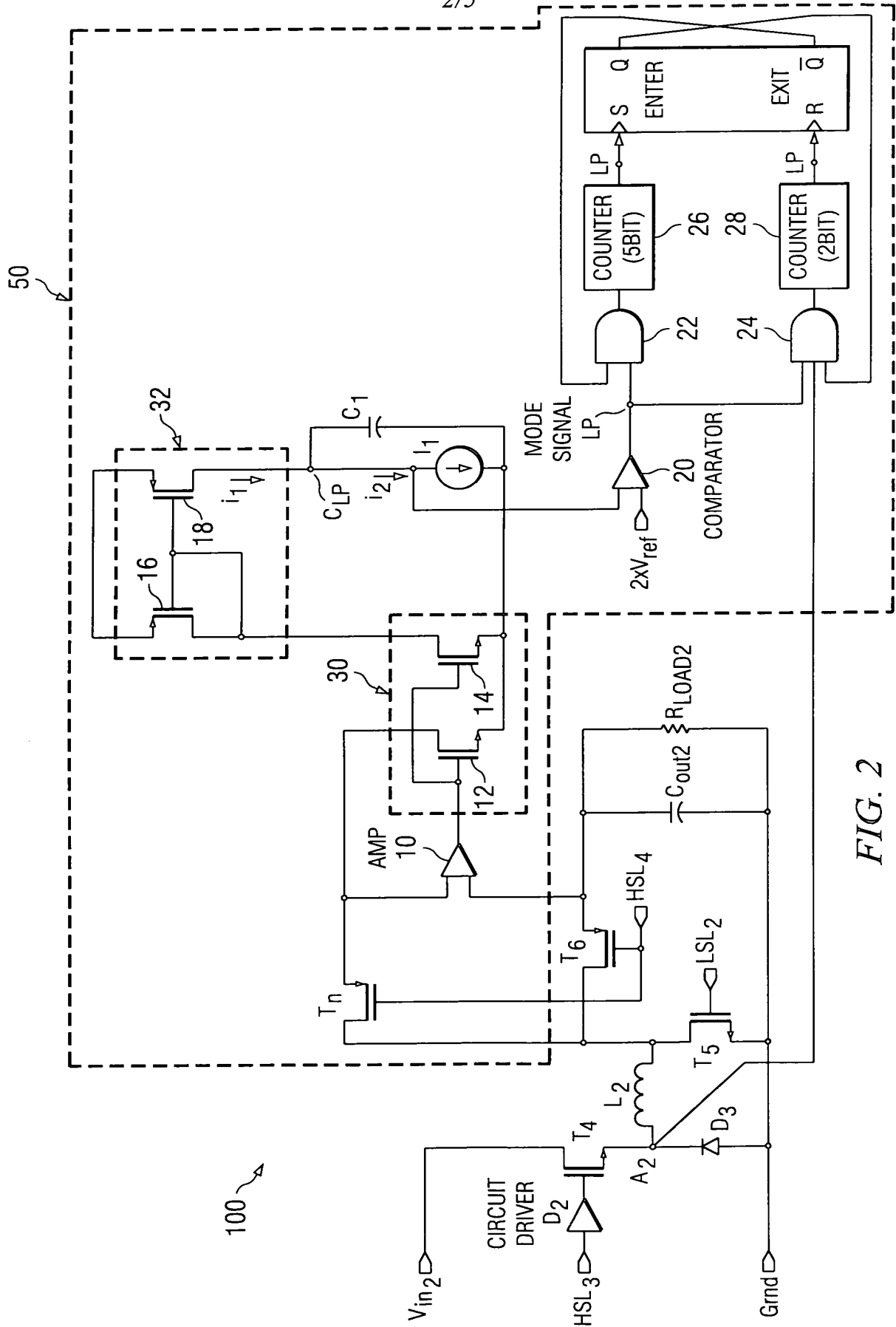
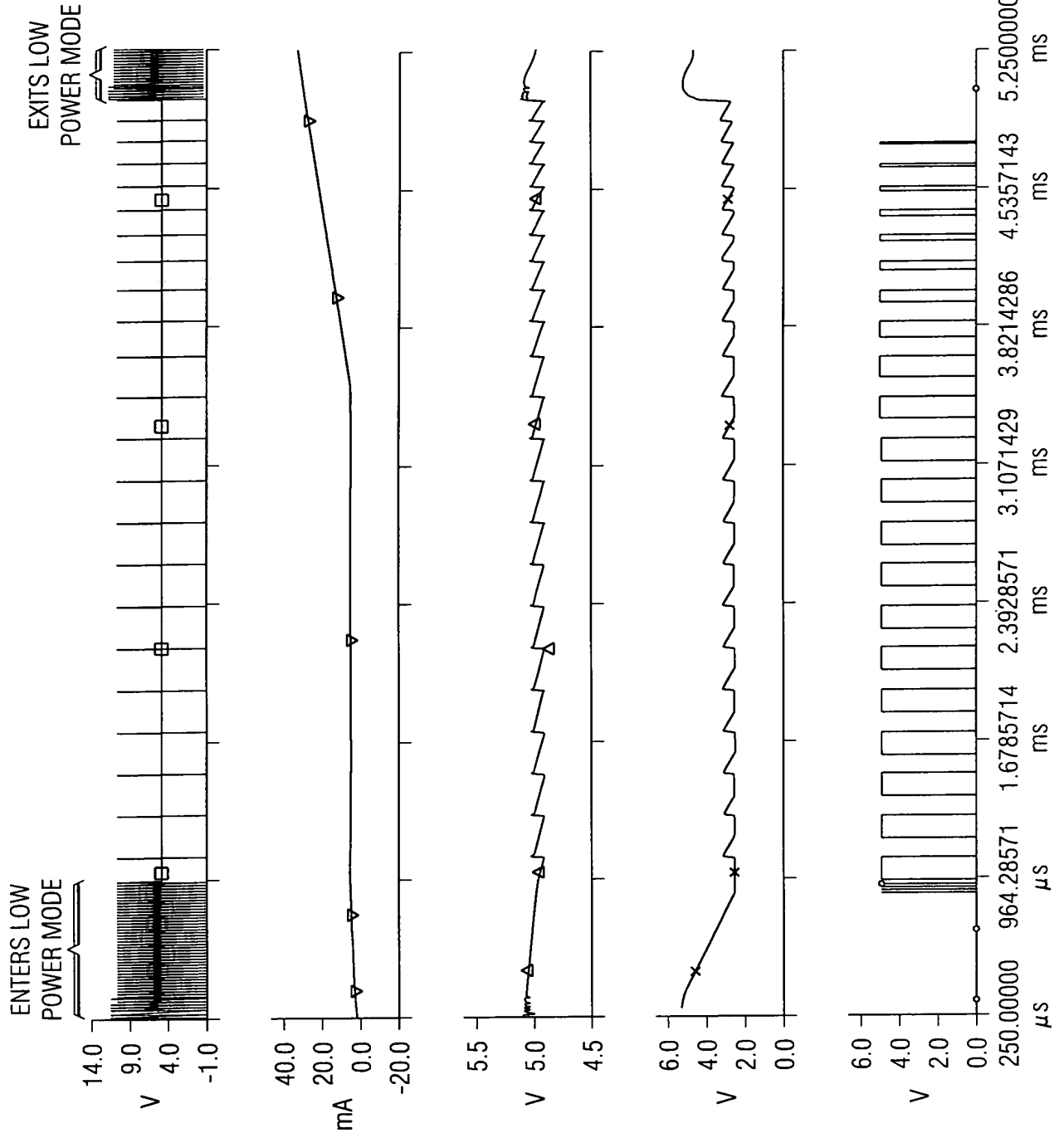


FIG. 2

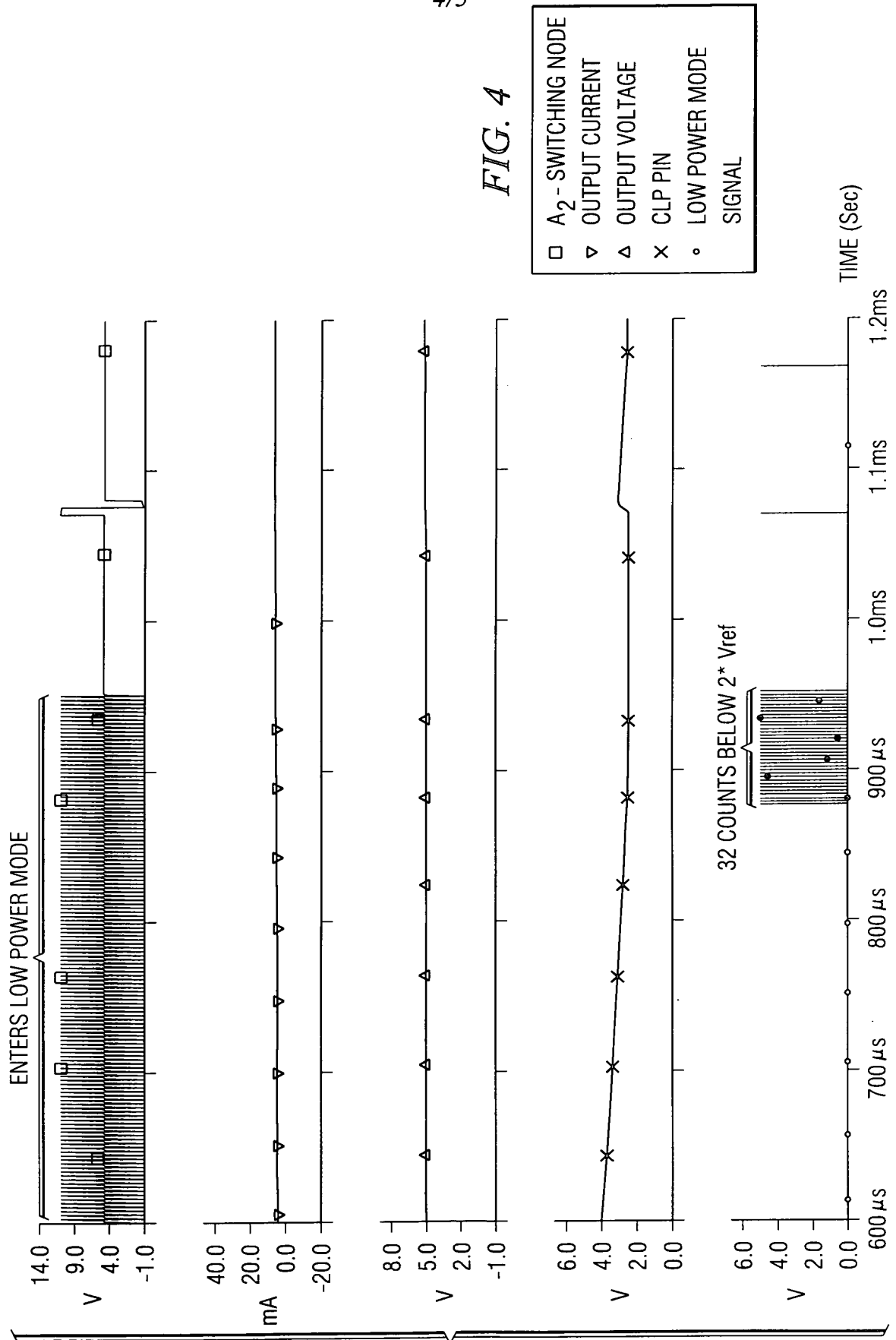
3/5

FIG. 3



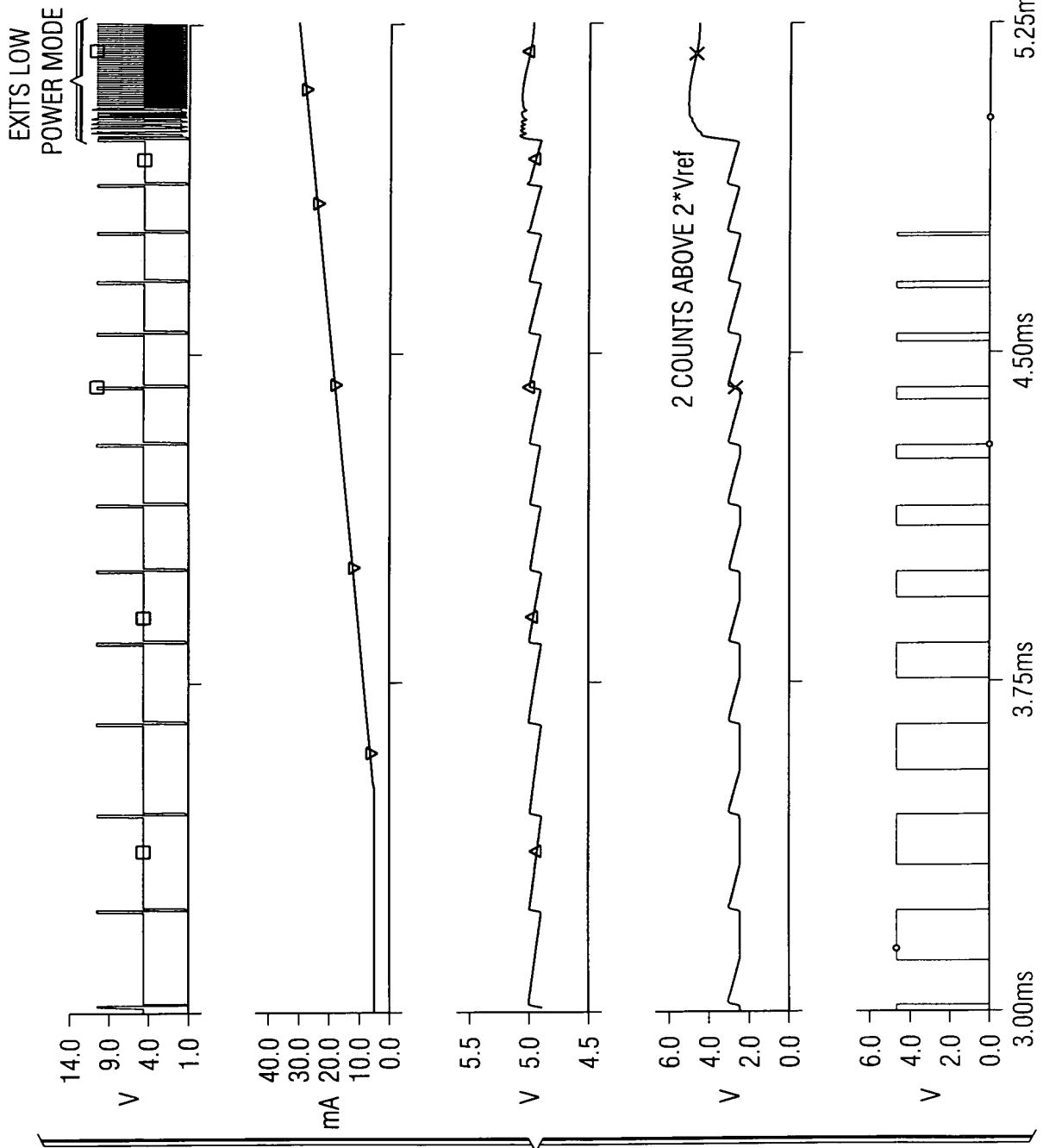
4/5

FIG. 4



5/5

FIG. 5



- A₂ - SWITCHING NODE
- ▽ OUTPUT CURRENT
- △ OUTPUT VOLTAGE
- × CLP PIN
- LOW POWER MODE SIGNAL